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# Securing the Future — an Information Flow Analysis of a Distributed OO Language

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**Abstract**

We present an information-flow type system for a distributed object-oriented language with active objects, asynchronous method calls and futures. The variables of the program are classified as high and low. We allow while cycles with high guards to be used but only if they are not followed (directly or through synchronization) by an assignment to a low variable. To ensure the security of synchronization, we use a high and a low lock for each concurrent object group (cog). In some cases, we must allow a high lock held by one task to be overtaken by another, if the former is about to make a low side effect but the latter cannot make any low side effects. This is necessary to prevent synchronization depending on high variables from influencing the order of low side effects in different cogs. We prove a non-interference result for our type system.

## 1 Introduction

The question of information security arises when the inputs and outputs of a program are partitioned into different security classes. In this case we want the high-security inputs not inappropriately influence the low-security outputs and other behaviour observable at low clearance. The strongest such property is non-interference [9] stating that there is no influence at all; or that variations in the high-security inputs do not change the observations at the low level.

Over the years, static analyses, typically type systems for verifying secure information flow have been proposed for programs written in many kinds of programming languages and paradigms — imperative or functional, sequential or parallel, etc. Each new construct in the language can have a profound effect on the information flows the programs may have. With the spread of distributed computing and multi-core processors, concurrent object-oriented programming is

gaining mindshare. The languages supporting this paradigm emphasize the greater independence of objects and various methods of communication between the objects and the concurrently running tasks. The effect these constructs have on the information flow has not yet been thoroughly investigated. On the one hand, the communication primitives are prone to introducing information leaks. On the other hand, the explicit independence of objects and their groups can provide clear evidence that certain flows are missing.

In this paper, we investigate a particular concurrent object-oriented language, related to Creol [12] and JCoBoxes [20]. The language, its syntax and semantics is almost the same as the concurrent OO sublanguage of the *core Abstract Behavioural Specification Language (ABS)* [10] and shall henceforth be named as this. At the core of this language lies the notion of a *concurrent object group* (cog). Different cogs run concurrently and independently of each other and communicate only via asynchronous method calls. When placing such a call, a *future* [8] (a placeholder for the eventually available return value) is immediately returned. A future admits certain operations for checking the presence of and reading the return value. Inside a cog, there may also be several running tasks sharing some common state (the fields of the object). In contrast, these tasks are scheduled cooperatively, such that there is always just a single active task per cog.

In this paper we propose a type system for checking the non-interference in programs written in ABS. For eliminating certain information flows, and for simplifying the checks we will fix or adjust certain details of the language in a manner that can be seen as non-essential for its purposes (specifying concurrent systems). Compared to [10], our language has a more fine-grained system of locks for controlling which task is currently running inside a cog. We have also restricted the scheduler for non-preemptive tasks, such that information flow properties are easier to enforce. The specification of scheduling decisions is made harder by the necessity to not introduce deadlocks into the program that were not there before. On the other hand, different cogs are running in a truly parallel fashion, scheduled nondeterministically.

We will introduce the syntax and semantics of ABS in Sec. 2. While describing the syntax, we will already introduce security types and annotations that form the basis for defining non-interference. In Sec. 3 we introduce our type system for secure information flow, and state and prove the properties satisfied by well-typed programs. We review the related work in Sec. 4 and discuss our results in Sec. 5. The appendix contains an example of a program in our language.

$$
\begin{array}{rll}
x \mid n \mid o \mid b \mid f & & \text{local variable} \mid \text{task} \mid \text{object} \mid \text{cog} \mid \text{field name} \\
Pr ::= \overline{Cl}\ B & & \text{program} \\
Cl ::= \mathsf{class}\ C\{\overline{T\ f}\ \overline{M}\} & & \text{class definition} \\
M ::= (m : (l, \overline{T}) \stackrel{l[,i]}{\to} \mathsf{Cmd}^l(T))(\overline{x})\ B & & \text{method definition} \\
B ::= \{\overline{T\ x}\ s; x\} & & \text{method body} \\
v ::= x \mid \mathsf{this} \mid \mathsf{this}.f & & \text{variable} \\
i ::= \ldots \mid -1 \mid 0 \mid 1 \mid \ldots & & \text{integer} \\
e ::= e_p \mid e_s & & \text{expression} \\
e_p ::= v \mid \mathsf{null} \mid i \mid e_p = e_p & & \text{pure expression} \\
e_s ::= e_p!_l m(\overline{e_p}) \mid e_p.\mathsf{get}_l \mid \mathsf{new}\ C \mid \mathsf{new\ cog}\ C & & \text{expression with side effects} \\
s ::= v := e \mid e \mid \mathsf{skip} \mid \mathsf{suspend}_l \mid \mathsf{await}_l\ g & & \text{statement} \\
\quad \mid \mathsf{if}\ (e_p)\ s\ \mathsf{else}\ s \mid \mathsf{while}_l\ (e_p)\ s \mid s; s & & \\
g ::= v? & & \text{guard} \\
l ::= L \mid H & & \text{security level} \\
\ell ::= l \mid i & & \text{security level or integer} \\
T ::= \mathsf{Int}_l \mid C_l \mid \mathsf{Fut}^\ell_l(T) \mid \mathsf{Guard}^\ell_l & & \text{security type}
\end{array}
$$

Figure 1: Syntax

# 2 The Programming Language

## 2.1 Syntax

Our programming language is a simplified version of ABS. Its (abstract) syntax is given in Fig. 1. The notation $\overline{X}$ denotes a sequence of $X$-s. Several constructs in the syntax are annotated by security levels. These do not have to be provided by the programmer, as they can be inferred automatically during type checking.

Let us explain the language constructs related to distributed execution. $e_p!m(\overline{e_p})$ denotes the *asynchronous* call of the method $m$. The call immediately returns a future. The get-construct is used to read the value of that future, if it is available. If not, then get blocks. The suspend-statement suspends the current thread, it is used for non-preemptive scheduling inside a cog. The statement await $g$ suspends until the guard $g = v?$ becomes true, which happens when the future $v$ obtains a value.

## 2.2 Operational Semantics

We first define run-time configurations. The program at run time is a set of cogs (concurrent object groups), each of which contains a set of objects. Each object is related to zero or more tasks. The run-time configurations are as follows:

$$P ::= b[n_1, n_2] \mid o[b, C, \sigma] \mid n\,\langle b, o, \sigma, s\rangle \mid P \parallel P$$

Each cog is represented by its identifier $b$ and the state of its locks. Each cog has two locks—the low lock, which is owned by task $n_1$ (or is free if $n_1 = \bot$), and the high lock, which is owned by task $n_2$ (or is free if $n_2 = \bot$).

Each object is represented by its identifier $o$, its cog $b$, its class $C$, and its state $\sigma$ (the values of its fields). Each task is represented by its identifier $n$, its cog $b$, its object $o$, the statement $s$ that is yet to be executed in this task, and its state $\sigma$ (the values of its local variables).

The run-time syntax will have some additional constructs:

$$e_p ::= \ldots \mid n \mid o \qquad s ::= \ldots \mid \mathsf{grab}_l \mid \mathsf{release}_l \qquad a ::= \mathsf{null} \mid i \mid n \mid o$$

Thus task and object identifiers can be used (these result from evaluating other expressions) and we will use $a$ to denote fully reduced expressions (i.e. constants): Also separate statements are introduced for grabbing and releasing locks (used for executing suspend and when starting and terminating tasks).

The initial configuration for the program $\overline{Cl}\ \{\overline{T\,x}\ s; x_0\}$ will be

$$b_0[n_0, n_0] \parallel n_0\,\langle b_0, \mathsf{null}, \sigma, s; \mathsf{release}_L; x_0\rangle$$

i.e. an initial cog $b_0$ will be created for the task $n_0$ executing the main method. This task will have both locks initially and the statement $\mathsf{release}_L$ is added to release the locks before the task terminates. This task is the only task that is not tied to an object (all tasks created later will be tied to some object). The variable $x_0$ (which must have type $\mathsf{Int}_L$) will contain the return value of the program. Input can be given to the program through the initial values of the variables in $\sigma$. These variables must be declared in the body of the main method.

Now we can give the reduction rules (including the necessary reduction contexts) in Figures 2 and 3. Again, some explanations are in order. The commands grab and release manipulate the locks of a cog. Suspending a task is equivalent to releasing a lock and then trying to grab it again. A method body that starts with a grab is currently suspended. It is possible to perform either a low or a high suspend. When a task has performed a high suspend, then only other high-suspended tasks can continue.

An asynchronous call (acall) creates a new task in the cog containing the receiver of the call. The new task is initially suspended. The name of the new task is used as the future.

$$R_1[e] ::= x := e \mid \mathsf{this}.f := e$$

$$R_2[e] ::= R_1[e] \mid \mathsf{if}\ (e)\ s_1\ \mathsf{else}\ s_2 \mid R_1[e.\mathsf{get}_l] \mid R_1[e!_l m(\overline{e'})] \mid$$
$$\mid R_1[e'!_l m(\overline{e_1}\, e\, \overline{e_2})] \mid R_2[e = e'] \mid R_2[e' = e]$$

$$\frac{n' \text{ fresh} \qquad \mathrm{body}(m) = s(\bar{x}); x' \qquad s_{task} = \mathsf{grab}_l; s[\bar{a}/\bar{x}]; \mathsf{release}_l; x'}{o'[b', C, \sigma'] \parallel n\,\langle b, o, \sigma, R_1[o'!_l m(\bar{a})]; s\rangle \rightsquigarrow \\ \rightsquigarrow o'[b', C, \sigma'] \parallel n\,\langle b, o, \sigma, R_1[n']; s\rangle \parallel n'\,\langle b', o', \sigma_{init}, s_{task}\rangle}\ (\mathsf{acall})$$

$$\frac{o' \text{ fresh}}{n\,\langle b, o, \sigma, R_1[\mathsf{new}\ C]; s\rangle \rightsquigarrow n\,\langle b, o, \sigma, R_1[o']; s\rangle \parallel o'[b, C, \sigma_{init}]}\ (\mathsf{new})$$

$$\frac{b' \text{ fresh} \qquad o' \text{ fresh}}{n\,\langle b, o, \sigma, R_1[\mathsf{new\ cog}\ C]; s\rangle \rightsquigarrow n\,\langle b, o, \sigma, R_1[o']; s\rangle \parallel b'[\bot, \bot] \parallel o'[b', C, \sigma_{init}]}\ (\mathsf{newcog})$$

$$\frac{}{n\,\langle b, o, \sigma', R_1[n'.\mathsf{get}_l]; s\rangle \parallel n'\,\langle b', o', \sigma, x\rangle \rightsquigarrow \\ \rightsquigarrow n\,\langle b, o, \sigma', R_1[\sigma(x)]; s\rangle \parallel n'\,\langle b', o', \sigma, x\rangle}\ (\mathsf{get}_1)$$

$$\frac{}{n\,\langle b, o, \sigma', R_1[n'.\mathsf{get}_l]; s\rangle \parallel n'\,\langle b', o', \sigma, s'; x\rangle \rightsquigarrow \\ \rightsquigarrow n\,\langle b, o, \sigma', \mathsf{await}_l(n'?); R_1[n'.\mathsf{get}_l]; s\rangle \parallel n'\,\langle b', o', \sigma, s'; x\rangle}\ (\mathsf{get}_2)$$

$$\frac{}{n\,\langle b, o, \sigma, R_2[x]; s\rangle \rightsquigarrow n\,\langle b, o, \sigma, R_2[\sigma(x)]; s\rangle}\ (\mathsf{var})$$

$$\frac{}{o[b, C, \sigma] \parallel n\,\langle b, o, \sigma', R_2[\mathsf{this}.f]; s\rangle \rightsquigarrow o[b, C, \sigma] \parallel n\,\langle b, o, \sigma', R_2[\sigma(f)]; s\rangle}\ (\mathsf{field})$$

$$\frac{}{n\,\langle b, o, \sigma, a; s\rangle \rightsquigarrow n\,\langle b, o, \sigma, s\rangle}\ (\mathsf{dummyexpr})$$

$$\frac{}{n\,\langle b, o, \sigma, x := a; s\rangle \rightsquigarrow n\,\langle b, o, \sigma[x \mapsto a], s\rangle}\ (\mathsf{assignvar})$$

$$\frac{}{o[b, C, \sigma] \parallel n\,\langle b, o, \sigma', \mathsf{this}.f := a; s\rangle \rightsquigarrow o[b, C, \sigma[f \mapsto a]] \parallel n\,\langle b, o, \sigma', s\rangle}\ (\mathsf{assignfield})$$

$$\frac{}{n\,\langle b, o, \sigma, \mathsf{skip}; s\rangle \rightsquigarrow n\,\langle b, o, \sigma, s\rangle}\ (\mathsf{skip})$$

$$\frac{}{n\,\langle b, o, \sigma, \mathsf{suspend}_l; s\rangle \rightsquigarrow n\,\langle b, o, \sigma, \mathsf{release}_l; \mathsf{grab}_l; s\rangle}\ (\mathsf{suspend})$$

$$\frac{}{b[\bot, \bot] \parallel n\,\langle b, o, \sigma, \mathsf{grab}_L; s\rangle \rightsquigarrow b[n, n] \parallel n\,\langle b, o, \sigma, s\rangle}\ (\mathsf{grab_L})$$

$$\frac{}{b[n', \bot] \parallel n\,\langle b, o, \sigma, \mathsf{grab}_H; s\rangle \rightsquigarrow b[n', n] \parallel n\,\langle b, o, \sigma, s\rangle}\ (\mathsf{grab_H})$$

$$\frac{}{b[n, n] \parallel n\,\langle b, o, \sigma, \mathsf{release}_L; s\rangle \rightsquigarrow b[\bot, \bot] \parallel n\,\langle b, o, \sigma, s\rangle}\ (\mathsf{release_L})$$

$$\frac{}{b[n', n] \parallel n\,\langle b, o, \sigma, \mathsf{release}_H; s\rangle \rightsquigarrow b[n', \bot] \parallel n\,\langle b, o, \sigma, s\rangle}\ (\mathsf{release_H})$$

Figure 2: Reduction rules (part 1)

$$\frac{i \neq 0}{n\,\langle b, o, \sigma, \mathsf{if}\ (i)\ s_1\ \mathsf{else}\ s_2; s\rangle \rightsquigarrow n\,\langle b, o, \sigma, s_1; s\rangle}\ (\mathsf{if}_+)$$

$$\frac{}{n\,\langle b, o, \sigma, \mathsf{if}\ (0)\ s_1\ \mathsf{else}\ s_2; s\rangle \rightsquigarrow n\,\langle b, o, \sigma, s_2; s\rangle}\ (\mathsf{if}_-)$$

$$\frac{}{\begin{array}{c} n\,\langle b, o, \sigma, \mathsf{while}_l\ (e)\ s_1; s_2\rangle \rightsquigarrow \\ \rightsquigarrow n\,\langle b, o, \sigma, \mathsf{if}\ (e)\ (s_1; \mathsf{suspend}_l; \mathsf{while}_l\ (e)\ s_1)\ \mathsf{else}\ \mathsf{skip}; s_2\rangle \end{array}}\ (\mathsf{while})$$

$$\frac{}{n\,\langle b, o, \sigma', \mathsf{await}_l(n'?); s\rangle \parallel n'\,\langle b', o', \sigma, x\rangle \rightsquigarrow n\,\langle b, o, \sigma', s\rangle \parallel n'\,\langle b', o', \sigma, x\rangle}\ (\mathsf{await}_1)$$

$$\frac{}{\begin{array}{c} n\,\langle b, o, \sigma', \mathsf{await}_l(n'?); s\rangle \parallel n'\,\langle b', o', \sigma, s'; x\rangle \rightsquigarrow \\ \rightsquigarrow n\,\langle b, o, \sigma', \mathsf{suspend}_l; \mathsf{await}_l(n'?); s\rangle \parallel n'\,\langle b', o', \sigma, s'; x\rangle \end{array}}\ (\mathsf{await}_2)$$

$$\frac{\text{the next step of } s_1 \text{ is low and the task } n' \text{ is high-low}}{\begin{array}{c} n\,\langle b, o, \sigma', \mathsf{await}_H(n'?); s\rangle \parallel n'\,\langle b', o', \sigma, \mathsf{grab}_H; s'; x\rangle \parallel \\ \parallel n_1\,\langle b', o_1, \sigma_1, s_1\rangle \parallel b'[n_1, n_1] \rightsquigarrow \\ \rightsquigarrow n\,\langle b, o, \sigma', \mathsf{suspend}_H; \mathsf{await}_H(n'?); s\rangle \parallel n'\,\langle b', o', \sigma, s'; x\rangle \parallel \\ \parallel n_1\,\langle b', o_1, \sigma_1, \mathsf{grab}_H; s_1\rangle \parallel b'[n_1, n'] \end{array}}\ (\mathsf{await}_3)$$

Figure 3: Reduction rules (part 2)

A while-loop suspends after each iteration. Hence an infinite loop cannot stop the computation in the entire cog and cause information flows through non-termination in such manner. The semantics of the await-command is straightforward, except for the rule ($\mathsf{await}_3$). It is used to avoid certain deadlocks. See Sec. 3.1 for the definition of low and high-low tasks and further discussion. Basically, rule ($\mathsf{await}_3$) allows the task $n'$ to preemptively start running (and suspend the task $n_1$) if its final value is being waited for. In such manner, the possible non-termination of task $n_1$ cannot affect the termination behaviour of $n$ (the high-low task $n'$ always terminates).

# 3 Type System for Non-Interference

## 3.1 Types

The types in the type system are the following:

$$T ::= \mathsf{Int}_l \mid C_l \mid \mathsf{Fut}^{\ell}_l(T) \mid \mathsf{Guard}^{\ell}_l \mid \mathsf{Exp}^l(T) \mid \mathsf{Cmd}^l \mid \mathsf{Cmd}^l(T) \mid (l, \overline{T}) \xrightarrow{l[,i]} \mathsf{Cmd}^l(T)$$

Thus we can have integers, objects of class $C$, futures, guards, possibly non-terminating expressions, commands, commands (method bodies) returning a value,

$$l \leq l \qquad L \leq H \qquad \frac{l_2 \leq l_1 \qquad \ell_3 \leq \ell_4}{\mathsf{Guard}^{\ell_3}_{l_1} \leq \mathsf{Guard}^{\ell_4}_{l_2}} \qquad \frac{l_2 \leq l_1 \qquad \ell_3 \leq \ell_4 \qquad T_5 \leq T_6}{\mathsf{Fut}^{\ell_3}_{l_1}(T_5) \leq \mathsf{Fut}^{\ell_4}_{l_2}(T_6)}$$

$$\mathsf{Guard}^{i}_{H} \leq \mathsf{Guard}^{L}_{H} \qquad \frac{l_1 \leq l_2}{C_{l_1} \leq C_{l_2}} \qquad \frac{l_1 \leq l_2}{\mathsf{Int}_{l_1} \leq \mathsf{Int}_{l_2}} \qquad \frac{\gamma, l \vdash e : T}{\gamma, l \vdash e : \mathsf{Exp}^{L}(T)}$$

$$\frac{\gamma, l \vdash e : T_1 \qquad T_1 \leq T_2}{\gamma, l \vdash e : T_2} \qquad \frac{\gamma, l \vdash s : \mathsf{Cmd}^{l_1} \qquad l_1 \leq l_2}{\gamma, l \vdash s : \mathsf{Cmd}^{l_2}} \qquad \frac{\gamma, l_1 \vdash s : \mathsf{Cmd}^{l} \qquad l_1 \geq l_2}{\gamma, l_2 \vdash s : \mathsf{Cmd}^{l}}$$

Figure 4: Subtyping rules

and methods. Here the subscript represents the security level of the value. For integers and objects, this corresponds to the upper bound on the security levels of the inputs that may have affected this value. For futures and guards, this is the upper bound on the (control flow) information that may affect which task this future is referring to. The security level on top of the arrow of the method type corresponds to the minimum level of side effects this method is allowed to perform. If this is high, then the side effects of this method do not affect the low part of the computation. The level $l_0$ in the method type denotes the security level of the receiver of the method call (i.e., this-argument). The superscripts on the types are the upper bound on information that may affect whether this future, guard, expression, or command eventually returns a value or terminates. If this information is high, then the effects of any computation that follows are high, too.

We also define the security level corresponding to those security types that can be types of variables:

$$level(C_l) = l \qquad level(\mathsf{Int}_l) = l \qquad level(\mathsf{Fut}^{\ell'}_{l}(T)) = l \qquad level(\mathsf{Guard}^{\ell'}_{l}) = l$$

Thus $level(T)$ is the maximum context level where assignments to variables of type $T$ are allowed.

The typing rules are given in Figures 4, 5, and 6. The general shape of the typing rules is $\gamma, l \vdash X : T$, where $\gamma$ is the typing context giving the types of local variables, fields, and methods, $l$ is the current *security context* upper bounding the information that may have affected whether the execution reaches the current program point, $X$ is a typable quantity and $T$ is its type. For typing methods, there is no security context. Considering the meaning of sub- and superscripts in the types, the rules in Figures 4, 5, and 6 should be rather straightforward. A program $Pr$ is well typed if $\vdash Pr : ok$ is derivable.

We also allow an integer $i$ to be added to the security level of the context. This is used to guarantee termination for methods (corresponding to high-low tasks in Def. 2) where the security level of the context is higher than the security level of

$$\frac{\begin{array}{c}(\forall i)\ Cl_i = \mathsf{class}\ C_i\{T_{i1}\ f_{i1} \ldots T_{ir_i}\ f_{ir_i}\ M_{i1} \ldots M_{ik_i}\} \\ (\forall i,j)\ M_{ij} = (m_{ij} : T'_{ij})(\overline{x_{ij}})\ B_{ij} \\ \gamma = \{C_i.f_{ij} \mapsto T_{ij} \mid 1 \le i \le n, 1 \le j \le r_i\} \cup \\ \cup \{C_i.m_{ij} \mapsto T'_{ij} \mid 1 \le i \le n, 1 \le j \le k_i\} \\ (\forall i,j)\ \gamma \vdash M_{ij} : T'_{ij} \qquad B = \{\overline{T\ x}\ s\} \qquad \gamma, \overline{x : T}, L \vdash s : \mathsf{Cmd}^L(\mathsf{Int}_L)\end{array}}{\vdash Cl_1 \ldots Cl_n\ B : ok}\ (\mathsf{Prog})$$

$$\frac{\gamma, \overline{x : T}, \mathsf{this} : C_{l_0}, l \vdash s : \mathsf{Cmd}^{l_1}(T') \qquad l = l_1}{\gamma \vdash (m : (l_0, \overline{T}) \xrightarrow{l} \mathsf{Cmd}^{l_1}(T'))(\overline{x})\ \{\overline{T\ x}\ s\} : (l_0, \overline{T}) \xrightarrow{l} \mathsf{Cmd}^{l_1}(T')}\ (\mathsf{Method}_1)$$

$$\frac{\gamma, \overline{x : T}, \mathsf{this} : C_{l_0}, l, i \vdash s : \mathsf{Cmd}^{l_1}(T') \qquad l > l_1 \qquad i > 0}{\gamma \vdash (m : (l_0, \overline{T}) \xrightarrow{l,i} \mathsf{Cmd}^{l_1}(T'))(\overline{x})\ \{\overline{T\ x}\ s\} : (l_0, \overline{T}) \xrightarrow{l,i} \mathsf{Cmd}^{l_1}(T')}\ (\mathsf{Method}_2)$$

$$\frac{\gamma(C.f) \le T \qquad T \in \{C'_l, \mathsf{Int}_l, \mathsf{Fut}^{\ell_1}_l(T')\} \qquad \gamma, l_2 \vdash \mathsf{this} : C_l}{\gamma, l_2 \vdash \mathsf{this}.f : T}\ (\mathsf{Field})$$

$$\frac{\gamma(x) = T}{\gamma, l \vdash x : T}\ (\mathsf{Var}) \qquad \frac{}{\gamma, l \vdash \mathsf{null} : C_L}\ (\mathsf{Null}) \qquad \frac{}{\gamma, l \vdash i : \mathsf{Int}_L}\ (\mathsf{Int})$$

$$\frac{\begin{array}{c}\gamma, l \vdash e : C_{l_0} \qquad \gamma, l \vdash \overline{e} : \overline{T} \qquad \gamma(C.m) = l_0, \overline{T} \xrightarrow{l} \mathsf{Cmd}^{l_1}(T_2) \\ l_0 \ge l \qquad \overline{T} \ge l \qquad l_1 = l\end{array}}{\gamma, l \vdash e!_l m(\overline{e}) : \mathsf{Fut}^{l_1}_l(l \vee l_1 \vee T_2)}\ (\mathsf{ACall}_1)$$

$$\frac{\begin{array}{c}\gamma, l \vdash e : C_{l_0} \qquad \gamma, l \vdash \overline{e} : \overline{T} \qquad \gamma(C.m) = l_0, \overline{T} \xrightarrow{l,i} \mathsf{Cmd}^{l_1}(T_2) \\ l_0 \ge l \qquad \overline{T} \ge l \qquad l_1 < l\end{array}}{\gamma, l \vdash e!_l m(\overline{e}) : \mathsf{Fut}^{i}_l(l \vee l_1 \vee T_2)}\ (\mathsf{ACall}_2)$$

$$\frac{\gamma, l \vdash e : \mathsf{Fut}^{l_1}_l(T)}{\gamma, l \vdash e.\mathsf{get}_l : \mathsf{Exp}^{l_1}(T)}\ (\mathsf{Get}_1) \qquad \frac{\gamma, l, i \vdash e : \mathsf{Fut}^{i_1}_l(T) \qquad i_1 < i}{\gamma, l, i \vdash e.\mathsf{get}_l : \mathsf{Exp}^{L}(T)}\ (\mathsf{Get}_2)$$

$$\frac{}{\gamma, L \vdash \mathsf{new}\ C : C_L}\ (\mathsf{New}) \qquad \frac{}{\gamma, L \vdash \mathsf{new\ cog}\ C : C_L}\ (\mathsf{NewCog})$$

$$\frac{\gamma, l \vdash e : \mathsf{Exp}^{l_2}(T)}{\gamma, l \vdash e : \mathsf{Cmd}^{l_2}}\ (\mathsf{DummyExpr}) \qquad \frac{}{\gamma, l \vdash \mathsf{skip} : \mathsf{Cmd}^{L}}\ (\mathsf{Skip})$$

$$\frac{\gamma(x) = T \qquad level(T) = l \qquad \gamma, l \vdash e : \mathsf{Exp}^{l_2}(T)}{\gamma, l \vdash x := e : \mathsf{Cmd}^{l_2}}\ (\mathsf{AssignVar})$$

$$\frac{\gamma(C.f) = T \qquad level(T) = l \qquad \gamma, l \vdash e : \mathsf{Exp}^{l_2}(T) \qquad \gamma, l \vdash \mathsf{this} : C_l}{\gamma, l \vdash \mathsf{this}.f := e : \mathsf{Cmd}^{l_2}}\ (\mathsf{AssignField})$$

Figure 5: Type rules (part 1)

$$\frac{\gamma, l \vdash e : \mathsf{Guard}_l^{l_1}}{\gamma, l \vdash \mathsf{await}_l(e) : \mathsf{Cmd}^{l_1}}\ (\mathsf{Await_1}) \qquad \frac{\gamma, l, i \vdash e : \mathsf{Guard}_l^{i_1} \qquad i_1 < i}{\gamma, l, i \vdash \mathsf{await}_l(e) : \mathsf{Cmd}^{L}}\ (\mathsf{Await_2})$$

$$\frac{}{\gamma, l \vdash \mathsf{grab}_l : \mathsf{Cmd}^{L}}\ (\mathsf{Grab}) \qquad \frac{}{\gamma, l \vdash \mathsf{release}_l : \mathsf{Cmd}^{L}}\ (\mathsf{Release})$$

$$\frac{}{\gamma, l \vdash \mathsf{suspend}_l : \mathsf{Cmd}^{L}}\ (\mathsf{Suspend})$$

$$\frac{\gamma, l \vdash e : \mathsf{Int}_l \qquad \gamma, l \vdash s_1 : \mathsf{Cmd}^{l_1} \qquad \gamma, l \vdash s_2 : \mathsf{Cmd}^{l_1}}{\gamma, l \vdash \mathsf{if}\ (e)\ s_1\ \mathsf{else}\ s_2 : \mathsf{Cmd}^{l_1}}\ (\mathsf{If})$$

$$\frac{\gamma, l \vdash e : \mathsf{Int}_l \qquad \gamma, l \vdash s : \mathsf{Cmd}^{l}}{\gamma, l \vdash \mathsf{while}_l\ (e)\ s : \mathsf{Cmd}^{l}}\ (\mathsf{While})$$

$$\frac{\gamma, l \vdash s_1 : \mathsf{Cmd}^{l_1} \qquad \gamma, l \vee l_1 \vdash s_2 : \mathsf{Cmd}^{l_2}}{\gamma, l \vdash s_1; s_2 : \mathsf{Cmd}^{l_1 \vee l_2}}\ (\mathsf{Seq_1})$$

$$\frac{\gamma, l \vdash s_1 : \mathsf{Cmd}^{l_1} \qquad \gamma, l \vee l_1 \vdash s_2 : \mathsf{Cmd}^{l_2}(T)}{\gamma, l \vdash s_1; s_2 : \mathsf{Cmd}^{l_1 \vee l_2}(T)}\ (\mathsf{Seq_2})$$

$$\frac{\gamma, l \vdash x : T \qquad level(T) = l}{\gamma, l \vdash x : \mathsf{Cmd}^{L}(T)}\ (\mathsf{ReturnVar}) \qquad \frac{\gamma, l' \vdash e : \mathsf{Fut}_l^{l_1}(T)}{\gamma, l' \vdash e? : \mathsf{Guard}_l^{l_1}}\ (\mathsf{Guard})$$

Figure 6: Type rules (part 2)

termination and thus while cycles are forbidden. Cycles could still occur through cycles in the await graph and to disallow this, each of these methods has a positive integer $i > 0$ and can only await after a task with a smaller integer. This makes the await graph of high-low tasks acyclic. To achieve this, we have some typing rules of the form $\gamma, l, i \vdash X : T$. The integer $i$ can also be assimilated with $\gamma$. For example, a rule

$$\frac{\gamma, l, i \vdash s_1 : \mathsf{Cmd}^{l_1} \qquad \gamma, l \vee l_1, i \vdash s_2 : \mathsf{Cmd}^{l_2}}{\gamma, l, i \vdash s_1; s_2 : \mathsf{Cmd}^{l_1 \vee l_2}}$$

is considered a special case of the rule ($\mathsf{Seq}_1$) and thus is not given separately. The integer $i$ is also used (instead of $L$) in the superscript of the futures and guards of high-low tasks.

By the next definition, we can now distinguish high and low reduction steps, depending on whether the reduced statement is typable in high context or not.

**Definition 1.** *Let the statement $s$ have the form $s_1; s_2$ where $s_1$ is not a sequential composition (because of associativity of the sequential composition operator, a statement always has either this form or the form $x$ (a single variable, which cannot be further reduced)). We call the next reduction step of $s$ a* high step *if $\gamma, H \vdash s_1 : \mathsf{Cmd}^l$ and a* low step *otherwise.*

The next definition allows to also distinguish high and low tasks. The previous and the next definition are used in the ($\mathsf{await}_3$) rule in Fig. 3.

**Definition 2.** *We call a task $n\,\langle b, o, \sigma, s; x\rangle$ a* high task *if $\gamma, H \vdash s : \mathsf{Cmd}^l$ and a* low task *otherwise. The high tasks are further distinguished: if $\gamma, H \vdash s : \mathsf{Cmd}^L$ then it is a* high-low task *and otherwise it is a* high-high task*.*

A high task can only make high steps, but a low task can make both high and low steps. A high-high task can contain only high cycles (cycles with a high guard) because low cycles are not allowed in high context. A high-low task cannot contain any cycles (because at most low guards are allowed but high context requires at least high guards). We have the following lemma.

**Lemma 1.** *A low task cannot contain high* while *cycles. A high-low task cannot contain any* while *cycles.*

The restriction on the use of high while cycles is modeled after the restriction in [21]. Thus no low steps can follow a high while cycle in the same task. This restriction is checked in the rules ($\mathsf{Seq}_1$) and ($\mathsf{Seq}_2$). Because a low task must eventually release both locks, which is a low step, a low task cannot contain high while cycles at all. We extend the same restriction to await cycles. Thus a low task cannot await after a task that is allowed to make high cycles.

In our language, the scheduler of a cog cannot switch to a different task before the current task releases the high lock (or both locks). This can be done explicitly using suspend, but it is also done implicitly after each iteration of a while or await cycle. In contrast, in [21], by default the scheduler can switch tasks at any time, this can be disallowed by wrapping a sequence of commands in a protect construct. The protect construct is not allowed to contain cycles. This restriction corresponds to our implicit suspend after each iteration of a cycle.

Because our language allows more than one cog, there can be several low tasks running in parallel (at most one in each cog). This can create a situation where a low task $n_1$ in one cog ($b_1$) is in high context and awaits for a high task $n_2$ in another cog ($b_2$) but the high lock of cog $b_2$ is held by a low task $n_3$ in cog $b_2$. Thus the task $n_1$ cannot make the next low step before the task $n_2$ terminates, which cannot happen before the task $n_3$ release the high lock but $n_3$ may make some low steps before it releases the lock. Thus it may depend on the high variables in $n_1$ whether low steps must be made in $n_3$ before the next low step in $n_1$ or not. Thus the low steps in $n_3$ are essentially in high context. To prevent this indirect information flow, we allow the task $n_2$ to overtake the high lock from $n_3$ in this situation. This means that $n_3$ is not required to make low steps before $n_1$ does, no matter what the values of high variables in $n_1$ are. This is handled by the reduction rule ($\mathsf{await_3}$).

## 3.2 Non-interference

We first define the low-equivalence relation in Fig. 7. Here we assume (w.l.o.g.) that all variables in the program have globally unique names. Thus we can use a single type context $\gamma$ instead of separate type contexts for each task.

From the definition of $\sim_\gamma$ we see that any typable command is equivalent to itself. Two commands are also equivalent if they both only have high side-effects. Commands with only high side-effects are also equivalent to skip-s.

Two local states are equivalent if the values of variables with low types are equal. Two objects are equivalent if the values of fields with low types are equal. The notion of equivalence is then extended to program configurations. We can now define the notion of non-interference we are considering. It is typical for the non-deterministic treatment of information flows, dating back to [23].

**Definition 3** (Non-interference)**.** *A program* $\overline{Cl}\ \{\overline{T\ x}\ s; x_0\}$ *is* non-interferent *if for any three states* $\sigma_0$, $\sigma_0^\bullet$ *and* $\sigma_1$ *satisfying* $\sigma_0 \sim_{\overline{x:T}} \sigma_1$,

$$b_0[n_0, n_0] \parallel n_0\,\langle b_0, \mathsf{null}, \sigma_0, s; \mathsf{release}_L; x_0\rangle \overset{*}{\rightsquigarrow} n_0\,\langle b_0, \mathsf{null}, \sigma_0^\bullet, x_0\rangle \parallel \ldots$$

*implies that there exists a state* $\sigma_1^\bullet$ *with* $\sigma_1^\bullet(x_0) = \sigma_0^\bullet(x_0)$ *and*

$$b_0[n_0, n_0] \parallel n_0\,\langle b_0, \mathsf{null}, \sigma_1, s; \mathsf{release}_L; x_0\rangle \overset{*}{\rightsquigarrow} n_0\,\langle b_0, \mathsf{null}, \sigma_1^\bullet, x_0\rangle \parallel \ldots \quad .$$

$$\frac{\gamma, l \vdash s : \mathsf{Cmd}^H}{s \sim_\gamma s} \qquad \frac{\gamma, H \vdash s : \mathsf{Cmd}^H \qquad \gamma, H \vdash s' : \mathsf{Cmd}^H}{s \sim_\gamma s'}$$

$$\frac{\gamma, l \vdash s : \mathsf{Cmd}^H(T)}{s \sim_\gamma s} \qquad \frac{\gamma, H \vdash s : \mathsf{Cmd}^H(T) \qquad \gamma, H \vdash s' : \mathsf{Cmd}^H(T)}{s \sim_\gamma s'}$$

$$\frac{\gamma, H \vdash s_1 : \mathsf{Cmd}^H \qquad s_2 \sim_\gamma s_2'}{s_1; s_2 \sim_\gamma s_2'} \qquad \frac{\gamma, H \vdash s_1 : \mathsf{Cmd}^H \qquad s_2 \sim_\gamma s_2'}{s_2 \sim_\gamma s_1; s_2'} \qquad \frac{s_2 \sim_\gamma s_2'}{s_1; s_2 \sim_\gamma s_1; s_2'}$$

$$\sigma \sim_\gamma \sigma' \equiv dom(\sigma) = dom(\sigma') \wedge \forall v \in dom(\sigma).\, level(\gamma(v)) = L \Rightarrow \sigma(v) = \sigma'(v)$$

$$b[n_1, n_2] \sim_\gamma b[n_1, n_2']$$

$$\frac{\sigma \sim_\gamma \sigma'}{o[b, C, \sigma] \sim_\gamma o[b, C, \sigma']} \qquad \frac{\sigma \sim_\gamma \sigma' \qquad s \sim_\gamma s'}{n\,\langle b, o, \sigma, s\rangle \sim_\gamma n\,\langle b, o, \sigma', s'\rangle} \qquad \frac{P_1 \sim_\gamma P_1' \qquad P_2 \sim_\gamma P_2'}{P_1 \parallel P_2 \sim_\gamma P_1' \parallel P_2'}$$

$$\frac{\gamma, H \vdash s : \mathsf{Cmd}^{l_1}(T_2) \qquad P \sim_\gamma P'}{n\,\langle b, o, \sigma, s\rangle \parallel P \sim_\gamma P'} \qquad \frac{\gamma, H \vdash s : \mathsf{Cmd}^{l_1}(T_2) \qquad P \sim_\gamma P'}{P \sim_\gamma n\,\langle b, o, \sigma, s\rangle \parallel P'}$$

Figure 7: The low-equivalence relation $\sim_\gamma$

Now we can prove the lemmas and the theorem for non-interference, stating that well-typed programs are non-interferent.

## 3.3 Lemmas

We first prove a number of lemmas related to the execution of a single task.

**Lemma 2.** *If $\gamma, l \vdash e : Int_L$ or $\gamma, l \vdash e : C_L$ then the value of $e$ (the result of its full reduction) does not depend on the high part of the state.*

**Lemma 3.** *If $P_1 \sim_\gamma P_2$ holds and $P_1 \rightsquigarrow P_1'$ and $P_2 \rightsquigarrow P_2'$ are low steps in the same task then $P_1' \sim_\gamma P_2'$.*

*Proof.* If the low lock is free in $P_1$ (and thus also in $P_2$) then the low steps must be grabbing of both locks. It is easy to see that $P_1 \sim_\gamma P_2$ implies $P_1' \sim_\gamma P_2'$ in this case.

Now we can assume that the low lock is held by a low task $n_1$ in $P_1$ (and thus also by the same task in $P_2$). Because the next step is a low step, the high lock must also be held by the task $n_1$ in $P_1$ and $P_2$. Let $P_1 = b[n_1, n_1] \parallel n_1\,\langle b, o, \sigma_1, s_1\rangle \parallel \ldots$ and $P_2 = b[n_1, n_1] \parallel n_1\,\langle b, o, \sigma_2, s_2\rangle \parallel \ldots$. Here $s_1 \sim_\gamma s_2$ and the next step in both $s_1$ and $s_2$ is a low step in cog $b$.

Let $s_1 = s_1'; s_1''$ and $s_2 = s_2'; s_2''$ where $s_1'$ and $s_2'$ are not sequential compositions. Then $\neg(\gamma, H \vdash s_1' : \mathsf{Cmd}^H)$ and $\neg(\gamma, H \vdash s_2' : \mathsf{Cmd}^H)$ and thus $s_1' = s_2'$. Thus in both $P_1$ and $P_2$ the next statement reduced is the same. Because $P_2 \sim_\gamma P_1$, the low parts of the state are equal in $P_2$ and $P_1$.

We can check for every possible construct that if the reduction of a statement $s$ depends on the high part of the state (this can only happen in rules ($\mathsf{get_1}$), ($\mathsf{get_2}$), ($\mathsf{var}$), ($\mathsf{field}$), ($\mathsf{await_1}$), ($\mathsf{await_2}$), and ($\mathsf{await_3}$)) then $\gamma, L \vdash s : \mathsf{Cmd}^H$ implies $\gamma, H \vdash s : \mathsf{Cmd}^H$ (i.e. this reduction would not be a low step; this would be a contradiction) and that the low side effect of the reduction cannot depend on the high part of the state. Here we use also Lemma 2.

Thus the next statement is reduced in the same way and has the same effect on the low part of the state in $P_2$ and $P_1$. Thus also the configurations after this step will be related by $\sim_\gamma$, i.e. $P_2' \sim_\gamma P_1'$. □

**Lemma 4** (Confinement)**.** *If $P \rightsquigarrow P'$ is a high step then $P' \sim_\gamma P$.*

*Proof.* We can consider the constructs one by one and check that the low variables, the low fields, and the status of the low lock of each cog are not changed if the step is a high step.

Also the low-equivalence class of statements is preserved. If the next statement is not fully reduced in one step (i.e. $s_1; s_2$ (where $s_1$ is not a sequential composition) reduces to $s_1'; s_2$) then it reduces to a high statement ($\gamma, H \vdash s_1'$). If the reduction creates a new task then the created task is a high task. □

**Lemma 5.** *If $P = b[n, n] \parallel \ldots$ then the task $n$ can eventually make a low step or (without making any low steps) release its high lock.*

*Proof.* We first define an upper bound on the number of steps that must be made in a statement (excluding steps used to reduce expressions to values) to release the high lock or make a low step or terminate.

$$\mathrm{ublor}(\mathsf{if}\ (e)\ s_1\ \mathsf{else}\ s_2) = 1 + \max(\mathrm{ublor}(s_1), \mathrm{ublor}(s_2))$$

$$\frac{s \text{ does not have the form } R_1[e'.\mathsf{get}_H]; s'}{\mathrm{ublor}(\mathsf{await}_H(e); s) = 3 + \mathrm{ublor}(s)}$$

$$\mathrm{ublor}(\mathsf{await}_H(e); R_1[e.\mathsf{get}_H]; s) = 4 + \mathrm{ublor}(s)$$

$$\mathrm{ublor}(R_1[e.\mathsf{get}_H]) = 5$$

$$\frac{s_1 \text{ is not an } \mathsf{if}, \mathsf{while}, \mathsf{await}, \mathsf{get}, \mathsf{suspend}, \mathsf{release}, \text{ or sequential composition}}{\mathrm{ublor}(s_1) = 1}$$

$$\mathrm{ublor}(\mathsf{suspend}_H; s) = \mathrm{ublor}(\mathsf{suspend}_H) = 2$$

$$\mathrm{ublor}(\mathsf{release}_H; s) = \mathrm{ublor}(\mathsf{release}_H) = 1$$

$$\frac{\neg(\gamma, H \vdash s_1 : \mathsf{Cmd}^H) \text{ and } s_1 \text{ is not a sequential composition}}{\mathrm{ublor}(s_1; s) = 1}$$

$$\frac{\gamma, H \vdash s_1 : \mathsf{Cmd}^l \text{ and } s_1 \text{ is not a } \mathsf{suspend}, \mathsf{release}, \mathsf{await}, \text{ or sequential composition}}{\mathrm{ublor}(s_1; s) = \mathrm{ublor}(s_1) + \mathrm{ublor}(s)}$$

Now we can use induction on $\mathrm{ublor}(s)$. If the next step in $s$ is a low step then we are done. If $s = \mathsf{release}_H; s_2$ then $P \rightsquigarrow P'$ in one step. Otherwise $\mathrm{ublor}(s) \geq 2$ and we can make a high step (or a finite number of high steps, if the next statement contains expressions, which must be reduced to their values first), which decreases $\mathrm{ublor}(s)$ by at least 1, does not touch the locks, and by Lemma 4 does not change the low-equivalence class of $P$. Thus we can reduce the proposition to the induction hypothesis. □

As next we show that a high-low task can always terminate. We stress that this denotes possibilistic termination only — there exists a successor configuration of the current program configuration where the task has terminated. Moreover, the execution path leading to the termination does not step out of the current equivalence class of $\sim_\gamma$.

**Lemma 6.** *If* $P = b[n_1, n_2] \parallel o[b, C, \sigma_1] \parallel n_2 \langle b, o, \sigma_2, s\rangle \parallel P_1$ *and* $\gamma, H \vdash s : \mathsf{Cmd}^L(T)$ *then* $P \rightsquigarrow^* P'$ *such that* $P' = b[n_1, \bot] \parallel o[b, C, \sigma_1'] \parallel n_2 \langle b, o, \sigma_2', x\rangle \parallel P_1'$, *where* $P \sim_\gamma P'$.

*Proof.* Because $n_2$ is a high-low task, it cannot contain any $\mathsf{while}$ cycles and it can await only after other high-low tasks (but the await graph of high-low tasks must be acyclic, i.e. a dag).

We first define an upper bound on the minimum number of steps that must be made in a statement (excluding steps made in the awaited tasks) to terminate.

$$\mathrm{ubterm}(\mathsf{if}\ (e)\ s_1\ \mathsf{else}\ s_2) = 1 + \max(\mathrm{ubterm}(s_1), \mathrm{ubterm}(s_2))$$

$$\mathrm{ubterm}(R_1[e.\mathsf{get}_H]) = 8$$

$$\mathrm{ubterm}(\mathsf{await}_H(e)) = 5$$

$$\mathrm{ubterm}(\mathsf{suspend}_H) = 3$$

$$\mathrm{ubterm}(x) = 0$$

$$\frac{s_1 \text{ is not an } \mathsf{if}, \mathsf{while}, \mathsf{await}, \mathsf{get}, \mathsf{suspend}, \text{ sequential composition, or variable}}{\mathrm{ubterm}(s_1) = 1}$$

$$\frac{s_1 \text{ is not a sequential composition}}{\mathrm{ubterm}(s_1; s) = \mathrm{ubterm}(s_1) + \mathrm{ubterm}(s)}$$

Now we can use induction on ubterm($s$). If $s = x$ then $P \rightsquigarrow^0 P'$ in zero steps, otherwise we can make a high step. If the next statement is of the form $R_1[e.\mathsf{get}_H]$, $\mathsf{await}_H(e)$, or $\mathsf{suspend}_H$ then we make 8, 5, or 3 high steps, respectively, after which ubterm($s$) has decreased by 8, 5, or 3, respectively. Otherwise we make one high step, which decreases ubterm($s$) by at least 1. In either case, by Lemma 4, the low-equivalence class of $P$ has not changed.

If the task $n_2$ suspends (without awaiting for another task) then the scheduler can immediately reactivate the task $n_2$.

If the task $n_2$ awaits for another high-low task (or tasks) then we use induction on the await dag to prove that all its tasks can terminate without making any low step in $P$. If necessary, a high task in the await dag can overtake the high lock of its cog before it is used to make a low step (the low task whose lock is overtaken may make a finite number of high steps before losing its high lock; depending on Lemma 5, the low task either releases its high lock explicitly (using $\mathsf{suspend}$ or $\mathsf{await}$) or its high lock is overtaken when it is about to make its next low step).

Thus we can reduce the proposition to the induction hypothesis. □

As next we show that a high task can always release its high lock while staying in the same equivalence class of $\sim_\gamma$. After that, a low step can be made in another (low) task.

**Lemma 7.** *If* $P = b[n_1, n_2] \parallel o[b, C, \sigma_1] \parallel n_2\,\langle b, o, \sigma_2, s\rangle \parallel P_1$ *and* $\gamma, H \vdash s : \mathsf{Cmd}^{l_1}(l_2)$ *then* $P \rightsquigarrow^* P'$ *such that* $P' = b[n_1, \bot] \parallel o[b, C, \sigma_1'] \parallel n_2\,\langle b, o, \sigma_2', s'\rangle \parallel P_1'$, *where* $\sigma_1 \sim_\gamma \sigma_1'$, $\sigma_2 \sim_\gamma \sigma_2'$, *and* $P_1 \sim_\gamma P_1'$.

*Proof.* We first define an upper bound on the number of steps that must be made in a statement to release the high lock or terminate.

$$\mathrm{ubrel}(\mathsf{if}\ (e)\ s_1\ \mathsf{else}\ s_2) = 1 + \max(\mathrm{ubrel}(s_1), \mathrm{ubrel}(s_2))$$

$$\mathrm{ubrel}(\mathsf{while}_l\ (e)\ s_1) = 4 + \mathrm{ubrel}(s_1)$$

$$\frac{s \text{ does not have the form } R_1[e'.\mathsf{get}_H]; s'}{\mathrm{ubrel}(\mathsf{await}_H(e); s) = 3 + \mathrm{ubrel}(s)}$$

$$\mathrm{ubrel}(\mathsf{await}_H(e); R_1[e.\mathsf{get}_H]; s) = 4 + \mathrm{ubrel}(s)$$

$$\mathrm{ubrel}(R_1[e.\mathsf{get}_H]) = 5$$

$$\frac{s_1 \text{ is not an } \mathsf{if}, \mathsf{while}, \mathsf{await}, \mathsf{get}, \mathsf{suspend}, \mathsf{release}, \text{ or sequential composition}}{\mathrm{ubrel}(s_1) = 1}$$

$$\mathrm{ubrel}(\mathsf{suspend}_l; s) = \mathrm{ubrel}(\mathsf{suspend}_l) = 2$$

$$\mathrm{ubrel}(\mathsf{release}_l; s) = \mathrm{ubrel}(\mathsf{release}_l) = 1$$

$$\frac{s_1 \text{ is not a } \mathsf{suspend}, \mathsf{release}, \text{ or sequential composition}}{\text{ubrel}(s_1; s) = \text{ubrel}(s_1) + \text{ubrel}(s)}$$

Now we can use induction on ubrel($s$). If $s = \mathsf{release}_H; s_2$ then $P \rightsquigarrow P'$ in one step. Otherwise ubrel($s$) $\geq 2$ and we can make a high step (or a finite number of high steps, if the next statement contains expressions, which must be reduced to their values first), which decreases ubrel($s$) by at least 1, does not touch the locks, and by Lemma 4 does not change the low-equivalence class of $P$. Thus we can reduce the proposition to the induction hypothesis. □

**Lemma 8.** *If $P = b[n_1, n_2] \parallel \ldots$ where $n_1 \neq \bot$ (but $n_2$ may be $\bot$) then $P \rightsquigarrow^* P' \rightsquigarrow P''$ where $P \sim_\gamma P'$ but $\neg(P' \sim_\gamma P'')$, and the step $P' \rightsquigarrow P''$ is made in cog $b$.*

*Proof.* By Lemma 7, any high task of cog $b$ can eventually release the high lock, after which the task $n_1$ can be activated, without using any low steps so far.

We first define an upper bound on the minimum number of steps that must be made in a statement (excluding steps made in the awaited tasks) to make a low step or terminate.

$$\text{ublow}(\mathsf{if}\ (e)\ s_1\ \mathsf{else}\ s_2) = 1 + \max(\text{ublow}(s_1), \text{ublow}(s_2))$$

$$\text{ublow}(R_1[e.\mathsf{get}_H]) = 8$$

$$\text{ublow}(\mathsf{await}_H(e)) = 5$$

$$\text{ublow}(\mathsf{suspend}_H) = 3$$

$$\frac{s_1 \text{ is not an } \mathsf{if}, \mathsf{while}, \mathsf{await}, \mathsf{get}, \mathsf{suspend}, \text{ or sequential composition}}{\text{ublow}(s_1) = 1}$$

$$\frac{\neg(\gamma, H \vdash s_1 : \mathsf{Cmd}^H) \text{ and } s_1 \text{ is not a sequential composition}}{\text{ublow}(s_1; s) = 1}$$

$$\frac{\gamma, H \vdash s_1 : \mathsf{Cmd}^l \text{ and } s_1 \text{ is not a sequential composition}}{\text{ublow}(s_1; s) = \text{ublow}(s_1) + \text{ublow}(s)}$$

Now use induction on ublow($s$) where $s$ is the statement of task $n_1$. If the next step in $s$ is a low step then we are done.

If the next statement is $\mathsf{suspend}_H$ then it can be reduced in 3 steps, decreasing ublow($s$) by 3, if the scheduler immediately reactivates the task $n_1$.

If the next statement is $\mathsf{await}_H$, which is used to await for another (high-low) task, then the high lock is released in 3 steps, then by Lemma 6 the awaited task can eventually terminate without causing any low steps, after which the task $n_1$ can be reactivated, and 2 more steps complete the reduction of the await statement. Thus the statement can be reduced in 5 steps, decreasing ublow($s$) by 5.

If the next statement is of the form $R_1[e.\mathsf{get}_H]$ then it first reduces to the statements $\mathsf{await}_H(e); R_1[e.\mathsf{get}_H]$, then in 5 steps back to $R_1[e.\mathsf{get}_H]$ but with the awaited task terminated, then two more steps (in addition to the reduction of $e$, if necessary) to complete the reduction. Thus it takes 8 steps to reduce the statement and ublow($s$) is decreased by 8.

Otherwise the next step is a high step which decreases ubterm($s$) by at least 1.

Thus, if the next step is a high step (which by Lemma 4 does not change the low-equivalence class of $P$) then we can reduce the proposition to the induction hypothesis. □

## 3.4 Theorems

Now we can prove the theorems.

**Theorem 9** (Subject reduction)**.** *If $P_1$ and $P_2$ are well typed under $\gamma$ and $P_1 \sim_\gamma P_2$ then if $P_1 \rightsquigarrow P_1'$ then there exists $P_2'$ such that $P_2 \rightsquigarrow^* P_2'$ and $P_1' \sim_\gamma P_2'$.*

*Proof.* We first consider the case where $P_1 \rightsquigarrow P_1'$ is a high step (grabbing or releasing of the high lock, any step by a task holding only the high lock, or a step in high context by a task holding both locks). In this case $P_1' \sim_\gamma P_1$ (by Lemma 4), thus also $P_2 \sim_\gamma P_1'$ and we can take $P_2' = P_2$ because $P_2 \rightsquigarrow^0 P_2$.

We now consider the case where $P_1 \rightsquigarrow P_1'$ is a low step in cog $b$ (grabbing or releasing both locks simultaneously or a step in low context by a task holding both locks).

If the step is grabbing of both locks then the low lock of $b$ must be free in $P_1$ and because of $P_1 \sim_\gamma P_2$ it must also be free in $P_2$. Let $P_1' = b[n_1, n_1] \parallel \ldots$, i.e. the scheduler has activated the task $n_1$, which then grabbed both locks. By Lemma 7, a high task in $P_2$ can release the high lock of cog $b$, if it is not already free. Now both locks of $b$ are free in $P_2$. Thus the scheduler can also activate the task $n_1$ in $P_2$, where it must also grab both locks, thus if $P_2 \rightsquigarrow P_2'$ then $P_2' = b[n_1, n_1] \parallel \ldots$ and by Lemma 3, $P_2' \sim_\gamma P_1'$ as required.

In the other case, both locks of $b$ are held (by the same task $n_1$) in $P_1$ and the low lock is held by $n_1$ in $P_2$. Using Lemma 8, we get $P_2 \rightsquigarrow^* P_2'' \rightsquigarrow P_2'''$, where $P_2'' \sim_\gamma P_1$ but $\neg(P_2''' \sim_\gamma P_2'')$. By Lemma 4, $P_2'' \rightsquigarrow P_2'''$ is not a high step. Thus $P_1 \rightsquigarrow P_1'$ and $P_2'' \rightsquigarrow P_2'''$ are both low steps and we can use Lemma 3 to get $P_1' \sim_\gamma P_2'''$. Thus we can take $P_2' = P_2'''$. □

**Theorem 10** (Non-interference)**.** *If $\vdash Pr : ok$, where $Pr = \overline{Cl}\ \{\overline{T\ x}\ s; x_0\}$, then $Pr$ is non-interferent.*

*Proof.* Let $\sigma_0$, $\sigma_0^\bullet$, and $\sigma_1$ be any three states satisfying $\sigma_0 \sim_{\overline{x:T}} \sigma_1$ and $P_0 \rightsquigarrow^* P^\bullet$ where

$$P_0 = b_0[n_0, n_0] \parallel n_0 \langle b_0, \mathsf{null}, \sigma_0, s; \mathsf{release}_L; x_0 \rangle$$
$$P^\bullet = n_0 \langle b_0, \mathsf{null}, \sigma_0^\bullet, x_0 \rangle \parallel \ldots$$

Here $P_0$ is the initial configuration of the program $Pr$ with initial state $\sigma_0$. Let

$$P_0 \rightsquigarrow P_1 \rightsquigarrow \ldots \rightsquigarrow P_k = P^\bullet$$

where $P_i$ are the intermediate configurations of the reduction sequence $P_0 \rightsquigarrow^* P^\bullet$.

Let $Q_0$ be the initial configuration of the program $Pr$ with initial state $\sigma_0$. We now use induction on $i$ to prove that for all $i \leq k$, $Q_0 \rightsquigarrow^* Q_i$ such that $P_i \sim_\gamma Q_i$ where $\gamma$ is the global type context of $Pr$. The base case holds because of $\sigma_0 \sim_\gamma \sigma_1$ (which implies $P_0 \sim_\gamma Q_0$). For the induction step we use theorem 9 to get from $P_i \sim_\gamma Q_i$ and $P_i \rightsquigarrow P_{i+1}$ and $Q_0 \rightsquigarrow^* Q_i$ to $P_{i+1} \sim_\gamma Q_{i+1}$ and $Q_0 \rightsquigarrow^* Q_i \rightsquigarrow^* Q_{i+1}$.

Thus we have $P_k \sim_\gamma Q_k$ and $Q_0 \rightsquigarrow^* Q_k$. Now $P_k \sim_\gamma Q_k$ implies that $Q_k$ must have the form

$$Q_k = n_0 \langle b_0, \mathsf{null}, \sigma_1^\bullet, x_0 \rangle \parallel \ldots$$

where $\sigma_0^\bullet \sim_\gamma \sigma_1^\bullet$. Because $x_0$ must have type $\mathsf{Int}_L$, it is a low variable and thus $\sigma_0^\bullet(x_0) = \sigma_1^\bullet(x_0)$. By definition 3, now $Pr$ is non-interferent. □

# 4 Related Work

The treatment of secure information flow in the language- and lattice-based setting is considered to have been pioneered by Denning and Denning. The first well-known type-based analysis for secure information flow in a simple imperative language was proposed by Volpano et al. [24]. Later, their analysis has been extended in many different directions, including treated language constructs, and the versatility of the tools for defining information flow properties. Our analysis, applied to a complex language, draws ideas from the developments in many of those directions. Let us give an overview of those.

While at first, the definitions of secure information flow were given in terms of distinguishable memories, bisimulation relations over program configurations [13, 17] soon emerged as a convenient and composable way of defining information flow properties. The use of weak bisimulations, allowing stuttering, appeared in [22].

Object-oriented features, including fields and methods, were first treated in the JFlow (Jif) compiler [14]. However, they did not provide formal non-interference results. Such results for an OO-language were provided in [3]. In that area, a lot

of attention has also been devoted to the analysis of low-level OO-languages, e.g. Java bytecode [4, 5].

Concurrent languages, with possible race conditions and synchronization primitives, bring their own challenges. Secure information flow in a language with the possibility to spawn new threads was first considered in [23]. Synchronization primitives were considered in [18]. A bisimulation-based definition of secure information flow was provided in [7]. In this area, most of the research seems to have concentrated on languages with parallel threads operating on a shared state. For the treatment of processes with private states, one may have to refer to the work based on process calculi [2, 11, 6]. Another interesting area is the building of distributed systems [25] satisfying certain information-flow properties.

In the analysis of thread pools with shared state, the properties of schedulers play a major role in the analysis of information flow properties. Their effect was first considered in [19]. More recently, scheduler strategies for providing the security of information flow have been considered [16, 15].

# 5 Conclusions

We have demonstrated a type-based information flow analysis for a rich modeling language that has been designed to be applicable in designing large systems. As such, the type-based technique is a suitable choice because of its efficiency in checking large artefacts.

Our work demonstrates that the notion of futures, heavily employed by the language, may cause some interesting information flows in the system. These information flows are particularly apparent if the futures are considered as first-class values. In particular, the synchronization points they create can interfere with the scheduling decisions. Our work shows that the details of scheduling in ABS may need some further design efforts.

Our analysis has been applied to a language employing many different features. Our work has been valuable in pointing out how these features interact with each other in terms of possible information flows. We believe that our work will be helpful in making information flow type systems more widely used in the design and programming phases of the software development process.

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# A An example

In Figures 8, 9, and 10 we have an example of a program in our language. This program gets as an input a value of 0 or 1 in the high variable *highInput* and leaks it into the low variable *leakedResult*, which is returned as low output from the program. This program would be typable if we allowed low side effects after a high cycle. In the current type system it is not typable because in the methods *start* a low method call appears after a high while cycle. The current example leaks only one bit of high data but the method *leak* can be called repeatedly to leak $n$ bits of high data in $O(n)$ time using only 4 cogs. This shows that the restriction on high while cycles is necessary.

```
class FalseLeaker{
    Int_H h
    (init : (L, Int_H) -L-> Cmd^L(Int_L))(hh){
        Int_L dummy
        h := hh; dummy}
    (start : (L, Leaker_L) -L-> Cmd^H(Int_L))(leaker){
        Int_L dummy
        while_H (h) skip;
        leaker!_L writeTrue(); dummy}
    (finish : (H,) -H-> Cmd^L(Int_H))(){
        Int_H dummy
        h := 0; dummy}
}
class TrueLeaker{
    Int_H h
    (init : (L, Int_H) -L-> Cmd^L(Int_L))(hh){
        Int_L dummy
        h := hh;
        dummy}
    (start : (L, Leaker_L) -L-> Cmd^H(Int_L))(leaker){
        Int_L dummy
        while_H (h = 0) skip;
        leaker!_L writeFalse();
        dummy}
    (finish : (H,) -H-> Cmd^L(Int_H))(){
        Int_H dummy
        h := 1;
        dummy}
}
```

Figure 8: An example of a program (part 1)

```
class Leaker{
    Int_L l
    Int_L trueWritten
    Int_L falseWritten
    FalseLeaker_L falseLeaker
    TrueLeaker_L trueLeaker
    (leak : (L, Int_H) -L-> Cmd^L(Int_L))(hh){
        Int_L result
        Fut^L_L(Int_L) fut
        trueWritten := 0; falseWritten := 0;
        if (falseLeaker = null) (falseLeaker := new cog FalseLeaker;
                                 trueLeaker := new cog TrueLeaker) else skip;
        fut := falseLeaker!_L init(hh); await_L(fut);
        fut := trueLeaker!_L init(hh); await_L(fut);
        falseLeaker!_L start(this); trueLeaker!_L start(this);
        while_L (trueWritten = 0) skip;
        while_L (falseWritten = 0) skip;
        result := l; result}
    (writeTrue : (L,) -L-> Cmd^L(Int_L))(){
        Int_L dummy
        Fut^L_H(Int_H) fut
        l := 1;
        fut := trueLeaker!_H finish(); await_H(fut);
        trueWritten := 1; dummy}
    (writeFalse : (L,) -L-> Cmd^L(Int_L))(){
        Int_L dummy
        Fut^L_H(Int_H) fut
        l := 0;
        fut := trueLeaker!_H finish(); await_H(fut);
        falseWritten := 1; dummy}
}
```

Figure 9: An example of a program (part 2)

$$
\begin{array}{l}
\{ \\
\quad \mathsf{Int}_H\ highInput \\
\quad \mathsf{Int}_L\ leakedResult \\
\quad Leaker_L\ leaker \\
\quad \mathsf{Fut}^L_L(\mathsf{Int}_L)\ fut \\
\quad leaker := \mathsf{new\ cog}\ Leaker; \\
\quad fut := leaker!_L leak(highInput); \\
\quad leakedResult := fut.get_L; \\
\quad leakedResult \\
\}
\end{array}
$$

Figure 10: An example of a program (part 3)